GDN-Traffic Classifier
10/40/100 Gbps packet switching

Description:
To provision Cloud Services in rack-scale networks, GDN-Traffic Classifier forwards packets and load balances traffic across numerous hosts. For frequently accessed tables, the Top-of-Rack (TOR) evenly distributes network traffic across all of the machines that can service the same request.

Algo-Logic’s Gateware Defined Networking® (GDN) Traffic Classifier can selectively forward packets from a 40 Gbps or 100 Gbps input port to multiple 10 Gbps or 40 Gbps endpoints within a Datacenter rack. Via software user-interface, rules can be programmed in the TOR to load the switching rules that determine how packets are switched and translated.

By using GDN Traffic Classifier rather than traditional x86/software switches, network operators can substantially increase their network performance with not only high throughput, but also reduce their network latency and power resulting in significantly lower Total Cost of Ownership (TCO).

Applications and Use-cases:
- Optimized for rack-scale cloud service provisioning
- Perfect for north/south packet forwarding in racks
- Ideal for Top-of-Rack (ToR) load balancing
- Supports Network Function Virtualization (NFV) in Software Defined Networking (SDN)

Key Features:
- Handles full line-rate Ethernet traffic from
  - From 40Gbps and 100 Gbps ports
  - To 40 Gbps and 10 Gbps ports
  - Up to 150 Million packets/seconds
- Deterministic, jitter-free packet processing
  - Sub-microsecond switching latency
- Supports multiple use case deployments
  - Network Address Translation (NAT)
  - 1-to-many packet forwarding
- Includes precise intra-rack latency monitoring
  - Measures delays to 6.4 nanoseconds
- Fully programmable under software control
- Consumes significantly low power

Implementation Platform
- Reference design fits in 1U rackmount chassis
- Portable gateware can be embedded in multiple types of customer end-products

Software Controller API Options
- Command-line and GUI Interfaces
- Standard C/C++, Java, Python, and multiple other language software APIs
- RESTful web service interface for SDN

To achieve this level of high performance, deterministic low latency, high throughput, and low power in datacenters networks, Algo-Logic designs 10/40/100 Gbps Traffic Classifiers that use its proprietary Associative Search Accelerator (ASA) and Hyper-speed Content Flow Manager (HCFM) GDN cores. Algo-Logic Systems 10/40/100 Gbps GDN Traffic Classifier solutions are available today.
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GDN-Traffic Classifier Reference Design Metrics:

<table>
<thead>
<tr>
<th>Feature</th>
<th>Description</th>
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<tbody>
<tr>
<td>Packet Forwarding Rate</td>
<td>Up to 150 MPPS (Million Packets Per Second) per EMSE2 core</td>
</tr>
<tr>
<td>Latency</td>
<td>Less than 1 microsecond (~45x less latency than with sockets)</td>
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<tr>
<td>Throughput</td>
<td>Line-rate network interface speeds of 40GE to 100GE per port</td>
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<tr>
<td>Power Consumption Rate</td>
<td>Less than 0.12 μ-Joules/packet (~100x less than with software sockets)</td>
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<tr>
<td>FPGA Devices Supported</td>
<td>Altera Stratix V A7, AB</td>
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<td>Platforms Supported</td>
<td>Altera Stratix V A7 100G Development Kit GX Edition, custom HW</td>
</tr>
<tr>
<td>Overall GDN Gain vs. Software</td>
<td>Gains between 100x to 1000x for datacenters, storage, ISPs/HSPs &amp; security industries</td>
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</tbody>
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GDN-Traffic Classifier Reference Design Architecture:

Ordering Codes:

AL-GDN-Traffic Classifier: (FPGA Device) (Platform) (Interface speed) (# Ports) (Memory): Example (with sample default):

- FPGA Device: (e.g.: Altera Stratix V, A7, AB)
- Platform: (Altera 100Gbps Stratix V GX Board, custom HW)
- Ethernet Interface Speed (10GE, 40GE and 100GE)
- Instances (Number of data ports per card: 4x SFPs, 2x QSFPs and 1 CFP)
- Memory: On-Chip RAM, QDRII, DDR3